IN THE SPECIFICATION

On page 9, line 10, of the specification, please insert the following brief descriptions for Figures 1-26 as follows:

--BRIEF DESCRIPTIONS OF THE DRAWINGS

A preferred exemplary embodiment of the present invention will hereinafter be described in conjunction with the appended drawings, wherein like designations denote like elements and:

Figure 1 illustrates one embodiment of the headset incorporating a noise cancellation system according to various aspects of the present invention;

Figures 2A-B depict front and sectional side views of one of the earpieces of the headset of Figure 1, respectively;

Figures 3 and 3A are block diagrams of control systems for the headset of Figure 1;

Figure 4 is a sectional view of the headset incorporating a pressure sensitive noise cancellation system according to various aspects of the invention;

Figure 5 is a schematic diagram of a switching system according to various aspects of the present invention;

Figure 6 is a schematic of diagram of a pressure sensitive noise cancellation control system according to various aspects of the present invention;

Figures 7 and 8 depict front and sectional side views, respectively, of a switched headset according to various aspects of the present invention;

Figures 9A-B are schematic diagrams of variable gain and pressure sensitive noise cancellation control systems according to various aspects of the present invention;

Figure 10 is a block diagram of a headset noise cancellation system using a subsonic acoustic signal according to various aspects of the invention;

Figure 11 is a schematic and block diagram of a variable gain control system for a noise cancellation system;

Figure 12 is a schematic and block diagram of the variable gain control system for a noise cancellation system employing phase lock loop circuits;

Figure 13 is a schematic and block diagram of a control system for a noise cancellation system including power control circuitry;

Figure 14 is a schematic and block diagram of a control system for a noise cancellation system including variable power control circuitry;

Figure 15 is a schematic diagram of the switched mode voltage generator suitable for use in the system to Figure 14;

Figure 16 is a schematic diagram of a power amplifier having variable voltage;

Figure 17 is a block diagram of a noise cancellation circuit according to various aspects of the present invention having a pass-through audio input signal;

Figure 18 is a block diagram of a noise cancellation system controller according to various aspects of the present invention suitable for use in a headset;

Figure 19 is a schematic diagram of an exemplary mixer circuit for use in the controller of Figure 18;

Figure 20 is a schematic diagram of an exemplary equalizer for use in the controller of Figure 18;

Figure 21 is a schematic diagram of an exemplary meeting circuit suitable for use in the controller of Figure 18;

Figure 22 is a schematic of an exemplary oscillator suitable for use in the controller of Figure 18;

Figure 23 is a schematic diagram of an exemplary power amplifier suitable for use in the controller of Figure 18;

Figure 24 is a schematic diagram of an exemplary error amplifier suitable for use in the controller of Figure 18;